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Nicole Teitler Cave	(Depositor's name)
<i>Nicole 25/e</i>	(Signature)
2/9/06	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/657,964	09/09/2003	Sriram Satakopan	004-8841	4133

TITLE OF INVENTION: LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	03/02/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
LEVIN, NAUM B	2825		716-006000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Sun Microsystems, Inc.

Santa Clara, CA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

- Issue Fee
 Publication Fee (No small entity discount permitted)
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 The Director is hereby authorized to charge the required fee(s), or credit any overpayment, to Deposit Account Number 50-0631 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature *Nicole 25/e*

Date 2/9/06Typed or printed name Nicole Teitler CaveRegistration No. 54,021

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**ZAGORIN O'BRIEN GRAHAM LLP***Intellectual Property Attorneys*7600B N. CAPITAL OF TEXAS HWY, SUITE 350
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To:	USPTO Central	Fax No.:	(512) 273-2885
Date:	February 9, 2006	Pages:	3 (including this sheet)
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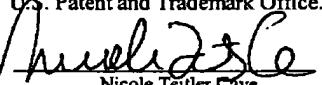
Title:	LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE		
Application No.:	10/657,964	Filed:	September 9, 2003
Examiner:	Levin, Naum B.	Group Art Unit:	2825
Atty. Docket No.:	004-8841	Confirmation No.:	4133

ATTACHED HERETO:

- (1) Issue Fee Transmittal (1 page)
- (2) Fee Address Indication Form (1 page)

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that, on the date shown below, this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office.

 2/9/06
Nicole Teitler Cave Date

Respectfully submitted,


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